



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei YAMAZAKI et al. Art Unit : 2871
Serial No. : 09/809,646 Examiner : Thoi V. Duong
Filed : March 16, 2001
Title : SEMICONDUCTOR DISPLAY DEVICE AND MANUFACTURING METHOD
THEREOF

Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Copies of the references listed on the attached form PTO-1449 are enclosed.

This statement is being filed before the receipt of a first Office action on the merits.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: April 11, 2003



John F. Hayden
Reg. No. 37,640

Fish & Richardson P.C.
1425 K Street, N.W.
11th Floor
Washington, DC 20005-3500
Telephone: (202) 783-5070
Facsimile: (202) 783-2331

40149127.doc

RECEIVED
APR 15 2003
TC 2800 MAIL ROOM